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We Claim:

A method of detecting one or more zombie global breakpoints for debugging computer software, said method including the steps of:

checking a breakpoint data structure to determine if a breakpoint known to a debugging process is at an address where a breakpoint fired;

if a known breakpoint cannot be determined at said address, verifying if a breakpoint condition continues to exist at the address where the breakpoint fired; and if said breakpoint condition does not exist, identifying said breakpoint as a zombie breakpoint.

- The method according to claim 1, wherein said verifying step includes the step 2. of checking that a special breakpoint instruction exists at said address, being the exception location.
- 3. The method according to claim 1, wherein said verifying step includes the step of checking that an illegal breakpoint instruction exists at said address, being the exception location.
- The method according to claim 1, wherein said verifying step includes the step 4. of checking that said address, being the exception location, is present in a special debug register.
- The method according to claim 1, wherein physical settings for causing a 5. breakpoint exception at a particular location are detectable from a breakpoint handler.
- 6. The method according to claim 5, wherein breakpoint removal logic is provided that lifts a physical breakpoint instruction from a breakpoint location before 30 removing a breakpoint entry from said breakpoint data structure δf said debugging process.

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7. A computer-implemented apparatus for detecting one or more zombie global breakpoints for debugging computer software, said apparatus including:

a central processing unit for executing said computer software; memory for storing at least a portion of said computer software;

means for checking a breakpoint data structure to determine if a breakpoint known to a debugging process is at an address where a breakpoint fired;

means for, if a known breakpoint cannot be determined at said address, verifying if a breakpoint condition continues to exist at the address where the breakpoint fired; and means for, if said breakpoint condition does not exist, identifying said breakpoint as a zombie breakpoint.

8. The apparatus according to claim 7, wherein said verifying means includes means for checking that a special breakpoint instruction exists at said address, being the exception location.

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9. The apparatus according to claim 7, wherein said verifying means includes means for checking that an illegal breakpoint instruction exists at said address, being the exception location.

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10. The apparatus according to claim 7, wherein said verifying means includes means for checking that said address, being the exception location, is present in a special debug register.

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11. The apparatus according to claim 7, wherein physical settings for causing a breakpoint exception at a particular location are detectable from a breakpoint handler.

12. The apparatus according to claim 11, wherein breakpoint removal logic is provided that lifts a physical breakpoint instruction from a breakpoint location before removing a breakpoint entry from said breakpoint data structure of said debugging

- process.
- 13. A computer program product having a computer readable medium having a computer program recorded therein for detecting one or more zombie global breakpoints

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for debugging computer software, said computer program product including:

computer program code means for checking a breakpoint data structure to determine if a breakpoint known to a debugging process is at an address where a breakpoint fired;

computer program code means for, if a known breakpoint cannot be determined at said address, verifying if a breakpoint condition continues to exist at the address where the breakpoint fired; and

computer program code means for, if said breakpoint condition does not exist, identifying said breakpoint as a zombie breakpoint.

- 14. The computer program product according to claim 13, wherein said computer program code means for verifying includes computer program code means for checking that a special breakpoint instruction exists at said address, being the exception location.
- 15. The computer program product according to claim 13, wherein said computer program code means for verifying includes computer program code means for checking that an illegal breakpoint instruction exists at said address, being the exception location.
- 16. The computer program product according to claim 13, wherein said computer program code means for verifying includes computer program code means for checking that said address, being the exception location, is present in a special debug register.
- 17. The computer program product according to claim 13, wherein physical settings for causing a breakpoint exception at a particular location are detectable from a breakpoint handler.
- 18. The computer program product according to claim 17, wherein breakpoint removal logic is provided that lifts a physical breakpoint instruction from a breakpoint location before removing a breakpoint entry from said breakpoint data structure of said debugging process.

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